

XILINX, INC.
END USER LICENSE AGREEMENT

CAREFULLY READ THIS END USER LICENSE AGREEMENT ("AGREEMENT"). BY CLICKING THE "ACCEPT" OR "AGREE" BUTTON, OR OTHERWISE ACCESSING, DOWNLOADING, INSTALLING OR USING THE SOFTWARE, YOU AGREE ON BEHALF OF LICENSEE TO BE BOUND BY THIS AGREEMENT.

IF LICENSEE DOES NOT AGREE TO ALL OF THE TERMS AND CONDITIONS OF THIS AGREEMENT, DO NOT CLICK THE "ACCEPT" OR "AGREE" BUTTON OR ACCESS, DOWNLOAD, INSTALL OR USE THE SOFTWARE; AND IF LICENSEE HAS ALREADY OBTAINED THE SOFTWARE FROM AN AUTHORIZED SOURCE, PROMPTLY RETURN IT FOR A REFUND.

1. Definitions

"Authorization Codes" means any FLEXlm license key, license file, license manager, dongle or other key, code or information issued by (or on behalf of) Xilinx that is necessary to download, install, operate and/or regulate User access to the applicable Software.

"Bitstream" means a machine-executable, binary form of a core used to program a Xilinx Device.

"Licensee" means the individual, corporation or other legal entity to which Xilinx has issued a Seat as described herein.

"Order Confirmation" online checkout confirmation, or other written or electronic ordering document or record evidencing the license purchase between Licensee and Xilinx.

"User" means a specific human being who is identified by Licensee as a person who is authorized to use the applicable Software on behalf of Licensee. In cases where Licensee is an individual who has obtained a Seat from Xilinx for his/her individual use, Licensee and User are one and the same.

"Seat" means the right granted by Xilinx to Licensee to have the Software used by one User in accordance with the terms and conditions of this Agreement. Note: a "Seat" is either a node-locked Seat or a floating Seat, which is set by the Authorization Codes.

"Software" means any one or more of the Xilinx design tools listed immediately below, to the extent that Xilinx has issued to Licensee a Seat as described herein that enables use of such software tool(s), as well as Updates (as defined below), but excluding any files, programs or other materials governed by a separate license as mentioned in Section 3(b) (Third-Party and Open Source Licenses) below.

Vivado Lab Edition

"Subscription" means the license type issued to Licensee as specified in the Authorization Codes and/or Order Confirmation. Subscription includes time-limited Subscriptions (that expire according to the Authorization Codes and/or Order Confirmation) and Subscriptions that function as perpetual licenses when the Authorization Codes and/or Order Confirmation indicate no expiry.

"Subscription Tier" means the specific level of license to the Software purchased by Licensee as identified in the Authorization Codes and/or the Order Confirmation, and includes for each Tier any combination of: seat counts or concurrency limits, license model (node locked or floating), permitted features and number of Updates per year.

"Update" means any updates, bug fixes, maintenance releases and the like if any, to the Software that Xilinx may, in its sole discretion, make generally available to all of its then-current licensees of the Software during the Update Period (defined below).

"Update Period" means, with respect to each Seat issued in connection with this Agreement, a period of one (1) year from the first date of issuance to Licensee of the Authorization Codes for such Seat, or with respect to Subscription licenses, such other time period stated in the Authorization Codes.

"Xilinx" means Xilinx, Inc., a Delaware corporation, with a place of business at 2100 Logic Drive, San Jose, CA 95124.

"Xilinx Device" means a programmable logic device, including a field programmable gate array (FPGA) device or complex programmable logic device (CPLD), manufactured and marketed by or for Xilinx.

2. Authorization Codes. Only the specific items of Software for which Xilinx has issued to Licensee the applicable Authorization Codes are validly licensed under this Agreement. Licensee may contact Xilinx to obtain replacement Authorization Codes to supersede previously issued Authorization Codes as necessary to facilitate Licensee's continued valid and authorized use of the applicable Software due to such matters as employee turnover, hard drive failures or the purchase of new computers. At the time of obtaining replacement Authorization Codes, Licensee may be required to certify that the previously issued Authorization Codes and the Software governed thereby have been permanently deleted or destroyed. Licensee shall not alter, over-ride or disable the Authorization Codes, or seek or use replacement Authorization Codes for purposes of exceeding the number of Seats purchased by Licensee. Licensee shall maintain the Authorization Codes strictly confidential. Licensee shall be responsible for any failure of its Users to comply with the terms and conditions of this Agreement.

3. License Grants.

3.1 Xilinx Licenses. Subject to the terms and conditions of this Agreement, Xilinx hereby grants to Licensee the following personal, non-exclusive, non-transferable, non-sublicensable licenses (under Xilinx' intellectual property rights as embodied in the applicable Software) for the term specified in Section 12:

(1) Node-Locked (per-User) Seat. If Xilinx has issued to Licensee a (FLEXlm) node-locked Seat, then Licensee may allow the Software to be (a) installed on and accessed from only the specific machine(s) allowed by the applicable Authorization Codes, (b) used by only one User (at a time) for each one Seat for such Software that has been issued to Licensee by Xilinx, and (c) used for the sole purposes of developing, synthesizing, testing and verifying designs only for Xilinx Devices.

(2) Floating (concurrent-User) Seat. If Xilinx has issued to Licensee a (FLEXlm) floating Seat, then Licensee may allow the Software to be (a) installed on and accessed from any number of machines, (b) used by up to the number of concurrent Users that is equal to the number of Seats for such Software that have been issued to Licensee by Xilinx as determined by the applicable Authorization Codes, and (c) used for the sole purposes of developing, synthesizing, testing and verifying designs only for Xilinx Devices.

(3) Distributable Components License.

A. Certain Definitions. "Distributable Component(s)" means those files supplied by Xilinx that are contained within certain Software tools and that are identified by Xilinx in such tool or its associated documentation as either (a) an IP core (or a LogiCORE product), which is designed for use in programming functionality onto a Xilinx Device (hereafter a "Core"), (b) a primitive, macro, example instantiation code, instantiation template, schematic or other written or graphic instructions that are specifically designed for utilizing functionality that Xilinx has permanently instantiated in a Xilinx Device (hereafter a "Design Element"), or (c) a software driver (or a device driver), which is designed for use in enabling a Xilinx Device to interoperate with other independent software programs such as an operating system (hereafter a "Driver"). Distributable Components may be supplied by Xilinx in either (y) source code form, such as a Core or Design Element provided in VHDL or Verilog form or a Driver provided in C language source form (hereafter "Modifiable Form"), or (z) a format other than source code, such as a Core or Design Element provided in netlist form or encrypted netlist form or a Driver provided in compiled object code form (hereafter "Restricted Form"). For purposes of clarity, a "Core" as used herein includes only a "Core" provided by Xilinx in the Software tools listed above at no additional charge, and not any fee-based LogiCORE products which are licensed separately under other agreements.

B. Modification Rights. For Distributable Components supplied by Xilinx in Modifiable Form, Licensee may modify such Distributable Components, provided that such modifications are limited to enabling such Distributable Components to, in the case of a Core or Design Element, program only a Xilinx Device, and in the case of a Driver, enable only a Xilinx Device to interoperate with other independent software programs such as an operating system. Licensee shall have no right to modify a Distributable Component that is supplied by Xilinx in a Restricted Form.

C. Distribution Rights. Licensee may distribute Distributable Components (including when modified per Section 3(a)(3)B (Modification Rights) above), provided that either (a) such Distributable Components have been converted into a machine executable form, such as in the case of a Core or Design Element, a Bitstream, and in the case of a Driver, in compiled object code form; or (b) the recipient thereof has a valid license from Xilinx to use the same Software. In all cases Distributable Components shall be distributed only for use to program a Xilinx Device.

3.2 Third-Party and Open Source Licenses. Certain files, programs or other materials distributed in connection with the Software may originate from third-party licensors and/or are licensed to Licensee (not under the terms and conditions of this Agreement, but rather) pursuant to the terms and conditions of the applicable licenses that appear upon installation, acceptance

and/or activation of the Software and/or are contained or described in the corresponding release notes or other documentation or header or source files. Such files, programs or other materials may be subject to the terms of the GNU General Public License or other open source or free software licenses. Licensee agrees to carefully review and abide by the terms and conditions of these licenses to the extent that they govern such files, programs or other materials. Notwithstanding the foregoing, as between Xilinx and Licensee, to the maximum extent permitted by applicable law and if not prohibited by any such third-party and/or open source licenses, all such files, programs or other materials shall be deemed covered under Sections 10 (Disclaimers) and 11 (Limitation of Liability) and 14(c) (Governing Law) of this Agreement.

4. Restrictions.

(a) **Special Use Restrictions.** No right is granted hereunder to use the Software or any Bitstream generated by use of the Software to program or develop designs for non-Xilinx Devices; however, Licensee may port ASIC designs to Xilinx Devices for the purpose of prototyping and verification. Furthermore, the Hardware Manager tool contained within the Software may be used to program devices solely for prototype purposes, and Xilinx specifically disclaims any liability for results obtained from use of the Hardware Manager tool to program devices for production purposes.

(b) **General Restrictions.** Except only to the extent otherwise expressly allowed under Section 3 (License Grants) above (or under applicable laws notwithstanding these restrictions), Licensee is not licensed to, and agrees not to: (i) decompile, translate, reverse-engineer, disassemble, or otherwise reduce to human readable form the Software or the data files generated by the Software; (ii) transmit the Software or display the object code of the Software on any computer screen, or make any hard-copy memory dumps of the object code; (iii) publish or disclose the results of any benchmarking of the Software, or use such results for any other software development activities; (iv) make any copies of the Software, except to make one (1) copy of the Software in machine-readable form solely for backup purposes; (v) modify or prepare derivative works of the Software, in whole or in part; (vi) hypothecate, rent, lease, loan, lend, time-share, sublicense, distribute or otherwise transfer the Software to any other individual, corporation or other legal entity; (vii) remove, alter or obscure any product identification, ownership or intellectual property rights notices on or in the Software; or (viii) as of December 11, 2027, directly or indirectly distribute, sublicense, provide direct or remote access to, or otherwise make available any version of the Software expressly designated as not licensed for use in the European Union ("EU") to any User in the EU, or take any other action which results in Software expressly designated as not licensed for use in the EU to be subject to the EU Cyber Resilience Act.

5. Payment; Taxes.

(a) **Payment.** Xilinx may invoice Licensee for the license fees identified by Xilinx to Licensee in connection with this Agreement. Licensee shall pay Xilinx' invoices in full thirty (30) days after the invoice date, without any deduction, counterclaim or offset. Fees and billing cycles for time-limited Subscriptions are specified in Xilinx' invoices or the Order Confirmation. Overdue amounts shall accrue interest at the rate of one and one-half percent (1.5%) per month, or at the highest legal interest rate, if less. Notwithstanding the foregoing, Xilinx may require pre-payment or other security, depending on Xilinx' credit review or Licensee's payment history or changes in financial condition. If Licensee's procedures require that an invoice be submitted against a purchase order before payment can be made, Licensee will be responsible for issuing such purchase order thirty (30) days before the payment due date. In cases where license fees have been pre-paid under a Xilinx Productivity Advantage (XPA) or similar arrangement, the outstanding budget available to Licensee will be reduced by the amount of the license fee.

(b) **Transfer Taxes.** The amounts payable to Xilinx pursuant to this Agreement are exclusive of any sales or use or other taxes or governmental charges. Licensee shall be responsible for payment of all such taxes or charges, except for any taxes based solely on Xilinx's net income. If applicable, Licensee will provide Xilinx with valid certification of any exemption from tax or reduced rate of tax imposed by any applicable taxing authority at the time of order.

(c) **Withholding Taxes.** If Licensee is required by applicable law to apply withholding taxes, Licensee shall provide Xilinx with advance notification of the requirement to apply such taxes and shall take all allowable actions requested by Xilinx that will reduce the withholding tax liability regarding transactions under this Agreement, including, but not limited to, applying on its own or on Xilinx' behalf for reduced withholding rates, concessionary tax rates or other favorable tax treatment. Licensee shall comply with applicable tax laws and regulations at its sole expense. Licensee will assist Xilinx' efforts to obtain allowable tax credits or deductions for withholding taxes by providing to Xilinx, in a form acceptable to Licensee, original or certified copies of tax payments receipts or other evidence of payment of taxes withheld by Licensee regarding payments made under this Agreement.

6. **Ownership.** Licensee acknowledges and agrees that all intellectual property and industrial rights in and to the Software and all copies thereof are and will remain the sole property of Xilinx (or its licensors, as applicable). Nothing contained in this Agreement will be construed as conferring by implication, estoppel or otherwise upon Licensee any license or other right except the licenses and rights expressly granted to Licensee in Section 3 (License Grants). The Software is protected by laws and international treaty

provisions covering intellectual property rights. With respect to any files, programs or other materials distributed in connection with the Software that have been licensed to Xilinx from third parties and which are not governed by a separate license as mentioned in Section 3(b) (Third-Party and Open Source Licenses) above, such third parties are intended third-party beneficiaries of the terms of this Agreement.

7. **Confidentiality.** Except as otherwise expressly permitted in Section 3 (License Grants), Licensee shall (a) maintain the confidentiality of the Software as the proprietary trade secrets of Xilinx (or its licensors, as applicable); and (b) not make the Software available in any form to any person other than to its Users who have a genuine "need to know" for purposes authorized by this Agreement, and who are bound by obligations of confidentiality no less protective of Xilinx (or its licensors, as applicable) than those contained herein. Licensee represents to Xilinx that it maintains a system of confidentiality consistent with commonly accepted practices to protect its own confidential business information, including written agreements with its Users, and that the Software will be protected by such a system to the same extent, but in no event with less than reasonable care. Licensee agrees that a breach of this Agreement may result in irreparable and continuing damage to Xilinx for which there may be no adequate remedy at law, and Xilinx shall be entitled to seek injunctive relief and/or a decree for specific performance, and such other relief (including monetary damages) as may be proper.

8. **Updates; Support.** Any Updates that are provided to Licensee shall be deemed and treated the same as the "Software" under this Agreement for which such Update applies. Also, any support (whether provided via the Xilinx technical support website or otherwise) for the Software provided to Licensee shall be governed by the terms and conditions of this Agreement.

9. **Limited Warranty and Sole Remedy.** Xilinx warrants that, for a period of ninety (90) days from the first date of issuance to Licensee of the Authorization Codes for a Seat (the "Warranty Period"), any media on which the Software may be furnished will, under normal use, be free from defects in material and workmanship. If Licensee reports in writing to Xilinx a breach of the foregoing warranty during the Warranty Period, Xilinx, at its option, after return of the defective software media, will either replace such media or refund the applicable license fee received by Xilinx and terminate this Agreement. The foregoing describes the sole liability of Xilinx and the exclusive remedy of Licensee with respect to any breach of the foregoing limited warranty.

10. DISCLAIMERS.

(A) EXCEPT FOR THE EXPRESS LIMITED WARRANTY SET FORTH IN SECTION 9 ABOVE, THE SOFTWARE (INCLUDING SUPPORT, IF ANY) IS PROVIDED "AS-IS" WITHOUT WARRANTY OF ANY KIND. XILINX AND ITS LICENSORS DISCLAIM ALL OTHER WARRANTIES AND CONDITIONS, EXPRESS, IMPLIED, STATUTORY OR OTHERWISE, INCLUDING WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, NON-INFRINGEMENT OF THIRD PARTY RIGHTS, AND FITNESS FOR A PARTICULAR PURPOSE. XILINX DOES NOT WARRANT THAT THE FUNCTIONS CONTAINED IN THE SOFTWARE WILL MEET LICENSEE'S REQUIREMENTS, OR THAT THE OPERATION OF THE SOFTWARE WILL BE UNINTERRUPTED OR ERROR FREE, OR THAT THE DEFECTS IN THE SOFTWARE WILL BE CORRECTED. FURTHERMORE, XILINX DOES NOT WARRANT OR MAKE ANY REPRESENTATIONS REGARDING USE OR THE RESULTS OF THE USE OF THE SOFTWARE (INCLUDING SUPPORT, IF ANY) IN TERMS OF CORRECTNESS, ACCURACY, RELIABILITY, OR OTHERWISE.

(B) LICENSEE ACKNOWLEDGES THAT USE OF THE SOFTWARE IN COMBINATION WITH OTHER FUNCTIONALITY, SOFTWARE OR PROTOCOLS MAY REQUIRE LICENSES FROM THIRD PARTIES AND LICENSEE ACCEPTS SOLE RESPONSIBILITY FOR OBTAINING SUCH LICENSES.

(C) THE SOFTWARE IS NOT DESIGNED OR INTENDED TO BE FAIL-SAFE, OR FOR USE IN CONNECTION WITH ANY APPLICATION REQUIRING FAIL-SAFE PERFORMANCE, SUCH AS IN LIFE-SUPPORT OR SAFETY DEVICES OR SYSTEMS, CLASS III MEDICAL DEVICES, NUCLEAR FACILITIES, APPLICATIONS RELATED TO THE DEPLOYMENT OF AIRBAGS, OR ANY OTHER APPLICATIONS THAT COULD LEAD TO DEATH, PERSONAL INJURY OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE (INDIVIDUALLY AND COLLECTIVELY, "CRITICAL APPLICATIONS"). FURTHERMORE, THE SOFTWARE IS NOT DESIGNED OR INTENDED FOR USE IN CONNECTION WITH ANY APPLICATIONS THAT AFFECT CONTROL OF A VEHICLE OR AIRCRAFT, UNLESS THERE IS A FAIL-SAFE OR REDUNDANCY FEATURE (WHICH DOES NOT INCLUDE USE OF SOFTWARE IN THE XILINX DEVICE TO IMPLEMENT THE REDUNDANCY) AND A WARNING SIGNAL UPON FAILURE TO THE OPERATOR. LICENSEE AGREES, PRIOR TO USING OR DISTRIBUTING ANY SYSTEMS THAT WERE DEVELOPED BY USE OF THE SOFTWARE OR THAT INCORPORATE ANY PORTION OF THE SOFTWARE, TO THOROUGHLY TEST THE SAME FOR SAFETY PURPOSES. TO THE MAXIMUM EXTENT PERMITTED BY APPLICABLE LAW, LICENSEE ASSUMES THE SOLE RISK AND LIABILITY OF ANY CRITICAL APPLICATIONS.

11. **LIMITATION OF LIABILITY.** TO THE MAXIMUM EXTENT PERMITTED BY APPLICABLE LAW: (1) IN NO EVENT SHALL XILINX OR ITS LICENSORS BE LIABLE FOR ANY LOSS OF DATA, LOST PROFITS, OR COST OF PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES, OR FOR ANY SPECIAL, INCIDENTAL, CONSEQUENTIAL OR INDIRECT DAMAGES ARISING UNDER, RELATED TO, OR IN CONNECTION

WITH THIS AGREEMENT OR THE USE OR OPERATION OF THE SOFTWARE, IN WHOLE OR IN PART, HOWEVER CAUSED AND UNDER ANY THEORY OF LIABILITY; (2) IN NO EVENT SHALL XILINX' OR ITS LICENSORS' ENTIRE LIABILITY ARISING UNDER, RELATED TO, OR IN CONNECTION WITH THIS AGREEMENT, EXCEED THE AMOUNT OF LICENSE AND SUPPORT FEES RECEIVED BY XILINX FROM LICENSEE FOR THE APPLICABLE SOFTWARE IN THE TWELVE (12) MONTHS IMMEDIATELY PRECEDING THE EVENTS GIVING RISE TO SUCH LIABILITY; (3) THESE LIMITATIONS AND EXCLUSIONS SHALL APPLY EVEN IF SUCH LOSS WAS REASONABLY FORESEEABLE OR IF XILINX HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES; AND (4) THESE LIMITATIONS SHALL APPLY NOTWITHSTANDING THE FAILURE OF THE ESSENTIAL PURPOSE OF ANY LIMITED REMEDIES HEREIN.

12. Term and Termination.

12.1 Term. If the Software licensed hereunder is only for trial or evaluation purposes (as set by the Authorization Codes), then (a) this Agreement and all rights hereunder shall automatically terminate on a date determined by the Authorization Codes (typically 30 or 60 or 90 days) or earlier upon notice by Xilinx, and (b) certain features and functions of the Software may be disabled by Xilinx during the trial/evaluation period. If the Software licensed hereunder is for a time-limited Subscription (as set by the Authorization Codes), this Agreement shall automatically terminate on the date determined by the Authorization Codes, unless earlier renewed by Licensee for a successive Subscription period. If the Software licensed hereunder is for a perpetual Subscription (as set by the Authorized Codes), this Agreement shall remain in effect until earlier terminated by Xilinx or Licensee. If the Software licensed hereunder is not a Subscription, this Agreement shall remain in effect until earlier terminated by Xilinx or Licensee.

12.2 Termination. Licensee may terminate this Agreement at any time by destroying the Software and all copies thereof. This Agreement will terminate immediately without notice from Xilinx if Licensee fails to comply with any of the terms and conditions herein. Upon termination of this Agreement for any reason, Licensee must destroy the Software and all copies thereof. The following provisions of this Agreement survive its termination: 1 (Definitions) and all defined terms, 2 (Authorization Codes), 3b (Third-Party and Open Source Licenses) to the extent not separately terminated according to the terms of such licenses, 4 (Restrictions), 5 (Payment; Taxes) for all unpaid amounts, 6 (Ownership), 7 (Confidentiality), 10 (Disclaimers), 11 (Limitation of Liability), 12 (Termination) and 14 (General).

13. Privacy; Data Collection. Xilinx may collect, use, and process diagnostic, technical, usage, and related information about the Software and Licensee's use of it, which may include target device characteristics (such as device family, density, package, or speed grade), system and application metadata, feature usage analytics, and other information reasonably necessary to provide, maintain, improve, and support the Software, verify compliance with this Agreement, and provide updates or technical support. Such information does not include Licensee's design files, source code, netlists (including design checkpoint files), or other proprietary design content, and does not include usernames, email addresses, IP addresses, or other information that directly identifies individual Users. All personal data collected by or on behalf of AMD in connection with the Software will be handled in accordance with the AMD Privacy Policy, available at <https://www.amd.com/privacy>. Further information about the diagnostic, technical, and usage information collected by the Software is described in the applicable Software documentation.

14. General.

(a) Governmental Use. The Software is comprised of commercial computer software developed exclusively at the expense of Xilinx. Accordingly, pursuant to the Federal Acquisition Regulations (FAR) Section 12.212 and Defense FAR Supplement (DFARS) Section 227.7202, use, duplication and disclosure of the Software by or for the U.S. government is subject to the restrictions set forth in this Agreement. Manufacturer is Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124.

(b) Export Compliance. Licensee shall adhere to all applicable import and export laws and regulations of Licensee's country and of the United States, without limitation. This Agreement may involve items and information that are subject to the International Traffic in Arms Regulations (ITAR) of the U.S. Department of State or Export Administration Regulations (EAR) of the U.S. Department of Commerce, Bureau of Industry and Security. The importer/exporter of record shall not export, reexport, resell, transfer, or disclose, directly or indirectly, any products or technical data, to any proscribed person, entity, or country, or foreign persons thereof, unless properly authorized by the U.S. government and/or any other applicable or relevant government or regulatory body.

(c) Export Restrictions. Licensee shall adhere to all applicable U.S. import/export laws and regulations including but not limited to the U.S. export Administration Regulations (EAR) and U.S. Office of Foreign Assets Control (OFAC), as well as the applicable import/export control laws and regulations of other countries. Licensee further agrees to not export, re-export, or transfer, directly or indirectly, any product, technical data, software or source code received from Xilinx under this Agreement, or the direct product of such technical data or software to any country for which the United States or any other applicable government requires an export

license or other governmental approval without first obtaining such licenses or approvals; or in violation of any applicable laws or regulations of the United States or the country where the technical data or software was obtained. Licensee acknowledges the technical data and software received will not, in the absence of authorization from U.S. or local law and regulations as applicable, be used by, exported, re-exported or transferred, directly or indirectly, to: (i) any sanctioned or embargoed country, or to nationals or residents of such countries; (ii) any restricted end-user as identified on any applicable government end-user list (e.g., BIS Denied Persons List); or (iii) any party where the end-use involves nuclear, chemical/biological weapons, rocket systems, or unmanned air vehicles; (iv) any party whose actions or functions are intended to support or contribute to the operation, installation, maintenance, repair, overhaul, refurbishing, development, or production of military items in Belarus, Russia, Burma, Cambodia, the People's Republic of China (PRC) or Venezuela (a "Military End User"); (v) any party for any military end-use or any end-use prohibited in Section 744 of the EAR, including but not limited to incorporation in a supercomputer (as defined in Section 772 of the EAR) or a component or equipment that will be used in a supercomputer any party any party for the design, development or production of a supercomputer located in PRC or Macau. For the most current Country Group listings, or for additional information about the EAR or Licensee's obligations under those regulations, refer to the U.S. Bureau of Industry and Security's website at <http://www.bis.doc.gov/>. For the most current ITAR regulations, visit https://www.pmddtc.state.gov/ddtc_public.

(c) **Governing Law.** This Agreement shall be governed by the laws of the State of California, without reference to conflict of laws principles.

(d) **Assignment.** Licensee shall not assign this Agreement or transfer any of the rights or obligations under this Agreement, in whole or in part, by operation of law or otherwise, without Xilinx' prior written consent. Any merger, acquisition, reorganization, change of control, or the like, involving Licensee shall be deemed an assignment in violation of the foregoing. Subject to the foregoing, this Agreement will be for the benefit of Xilinx and its successors and assigns, and will be binding on Licensee's permitted assignees.

(e) **Allocation of Risk.** Licensee acknowledges and agrees that each provision of this Agreement that provides for a disclaimer of warranties or an exclusion or limitation of damages represents an express allocation of risk, and is part of the consideration of this Agreement.

(f) **Waiver; Amendment.** No waiver, express or implied, by either party of any right or remedy for any breach by the other party of any provision of this Agreement will be deemed or construed to be a waiver of any succeeding breach of such provision or as a waiver of the provision itself or of any other breach or provision. No waiver of or amendment to this Agreement will be effective unless reduced to writing and executed by authorized representatives of the parties.

(g) **Severability.** If, for any reason, a court of competent jurisdiction finds any provision of this Agreement, or portion thereof, to be invalid, illegal or unenforceable, then such provision shall be enforced to the maximum extent permissible to carry out the intent of the parties, otherwise stricken, and the remainder of this Agreement shall continue in full force and effect.

(h) **Notices.** Any notices required or permitted by this Agreement shall be in writing and shall be delivered as follows, with notice deemed given as indicated: (i) by personal delivery, when delivered personally; (ii) by overnight courier, upon written verification of receipt; (iii) by facsimile transmission, upon acknowledgment of receipt of electronic transmission, provided that notice is also provided by one of the other methods herein within five (5) days thereafter; or (iv) by certified or registered mail, return receipt requested, upon verification of receipt. Notice shall be sent to the addresses provided by each party to the other in connection with this Agreement, or to such other address as either party may specify in writing. Notices to Xilinx shall be addressed to the attention of: Xilinx, Inc., Attn: General Counsel, Legal Department, 2100 Logic Drive, San Jose, CA 95124.

(i) **Entire Agreement.** This Agreement constitutes the entire agreement between the parties with respect to the Software, and supersedes all prior or contemporaneous discussions, understandings or agreements, written or oral, regarding the subject matter hereof. No additional terms or modifications proposed by Licensee shall be binding on Xilinx unless expressly agreed to in writing and signed by Xilinx. All terms and conditions of any purchase order or other document issued by Licensee in connection with this Agreement or the Software shall be void and of no force or effect to add to or modify this Agreement.

(j) **Interpretation.** By clicking to "accept" or "agree" to this Agreement, Licensee acknowledges and agrees that it has read and understood this Agreement, has had an opportunity to discuss this Agreement with its legal and other advisors, and agrees to be bound by the terms and conditions of this Agreement. This Agreement shall be interpreted fairly in accordance with its terms and without any strict construction in favor of or against either party.

2026.04.09